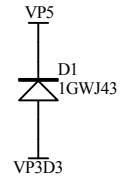


CQ-SH2A-CN1

1	A0
2	A1
3	A2
4	A3
5	A4
6	A5
7	A6
8	A7
9	
10	
11	
12	
13	
14	
15	
16	
17	A17
18	A18
19	A19
20	
21	GND
22	D0
23	D1
24	D2
25	D3
26	D4
27	D5
28	D6
29	D7
30	
31	
32	
33	
34	
35	
36	
37	
38	
39	
40	nCS2

CQ-SH2A-CN2

1	
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4	
5	
6	
7	
8	GND
9	GND
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	nRST
20	nWR0
21	
22	nRD
23	
24	A7
25	A6
26	A5
27	A4
28	A3
29	A2
30	A1
31	VP3D3
32	VP3D3
33	
34	
35	
36	
37	VP5
38	VP3D3
39	GND
40	



GND	CN5_64	GND
GND	CN5_63	GND
	CN5_62	IOP96_RDY
nRST	CN5_61	IOP97_CPURST_n
	CN5_60	IOP99_CS1_n
nCS2	CN5_59	IOP102_CS4_n
	CN5_58	IOP106_CS6_n
nCS4	CN5_57	IOP100_CS5_n
GND	CN5_56	GND
RESET IN n	CN5_55	GND
	CN5_54	RESOUT_n
	CN5_53	IP101_DACK1
	CN5_52	IP136_DACK0
A0	CN5_51	IOP107_MD6_IOIS16_n
	CN5_50	IOP108_A0
	CN5_49	I_STATU1
	CN5_48	IP110_BS_n
GND	CN5_47	I_STATU0
	CN5_46	GND
	CN5_45	IOP109_FROMRST_n
	CN5_44	IOP112_CS2_n
	CN5_43	IOP98
	CN5_42	IOP119_BREQ_n
	CN5_41	NC
	CN5_40	IP159_BACK_n
	CN5_39	IP148
GND	CN5_38	IP124
	CN5_37	GND
	CN5_36	IOP19_CS0_n
	CN5_35	IOP138_A20
	CN5_34	IP154_RY_BY
	CN5_33	IOP139_A21
	CN5_32	IOP83_A18
	CN5_31	NC
	CN5_30	IOP137_A19
GND	CN5_29	NC
	CN5_28	GND
	CN5_27	NC
	CN5_26	IOP140_A22
	CN5_25	IP142
	CN5_24	IOP144_BYTE_n
	CN5_23	IOP146_A16
	CN5_22	IOP145_A17
	CN5_21	IP174
GND	CN5_20	IOP3
	CN5_19	GND
	CN5_18	IOP2
	CN5_17	IP175
	CN5_16	IP184_GCLK9
	CN5_15	IP194
	CN5_14	IOP147_A12
	CN5_13	IOP150_A11
	CN5_12	IOP151_A10
GND	CN5_11	IOP152_A9
	CN5_10	GND
A7	CN5_9	IOP153_A8
A6	CN5_8	IOP162_A7
A5	CN5_7	IOP161_A6
A4	CN5_6	IOP160_A5
A3	CN5_5	IOP163_A4
A2	CN5_4	IOP164_A3
A1	CN5_3	IOP165_A2
VP3D3	CN5_2	IOP167_A1
VP3D3	CN5_1	VP3D3

CN2

1	VP3D3
2	IP91
3	IOP94
4	IOP93
5	IOP90
6	IOP89
7	GND
8	IOP82_GCLK2
9	GND
10	IOP78_DB8
11	IOP77_DB9
12	IOP76_DB10
13	IOP75_DB11
14	IOP74_DB12
15	IP169
16	IP72
17	IP71
18	IOP69_DB13
19	IOP68_DB15
20	GND
21	IOP64_WE1B_n
22	IOP65_WE0B_n
23	IOP55
24	IOP63_RDB_n
25	IOP60
26	IOP61
27	IP57
28	IP58
29	IP51
30	GND

UB4

CN4

30	GND
29	IOP13_AB11
28	IOP15_AB18
27	IOP16_AB12
26	IOP120_DB3
25	IOP122_DB4
24	IOP123_DB5
23	IOP126_DB6
22	IOP127_DB7
21	IOP128_WRB_n
20	GND
19	IOP129_AB16
18	IOP132_AB15
17	IOP133_AB14
16	IOP134_AB13
15	IOP135_AB17
14	IOP23_AB4
13	IOP202_CS1B_n
12	IOP25_AB3
11	IOP24_AB5
10	IOP29_AB1
9	IOP28_AB2
8	IOP31_DB1
7	IOP30_DB0
6	IOP34_DB14
5	IOP33_DB2
4	IOP49_AB7
3	IOP48_AB6
2	IOP50_AB8
1	VP3D3

CN3

1	VP3D3
2	VP3D3
3	CN3_3
4	CN3_4
5	CN3_5
6	CN3_6
7	CN3_7
8	CN3_8
9	CN3_9
10	CN3_10
11	CN3_11
12	CN3_12
13	CN3_13
14	CN3_14
15	CN3_15
16	CN3_16
17	CN3_17
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50	CN3_50
51	CN3_51
52	CN3_52
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56	CN3_56
57	CN3_57
58	CN3_58
59	CN3_59
60	CN3_60
61	CN3_61
62	CN3_62
63	CN3_63
64	CN3_64

MAIPRJ_CQSH2A_MAI-SP3E-D64

CQ-SH2AとMAI-SP3E-D64-M4との
接続図

電源は外部から+3.3V(VP3D3)を給電するため
CQ-SH2A基板のジャンパーパターンJP15カットする。

+3.3VレギュレータをU2を保護するために、ショットキー
ダイオードD1を追加

2010/09/02 修正 A17,A18,A19,nCS4追加

Title		
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A3		
Date:	2-Sep-2010	Sheet of
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